



深圳市福瑞达显示技术有限公司
SHENZHEN FRIDA LCD CO.,LTD

Doc.No.: FRD122B34001-B

REV : B

PAGE : 1/18

SPEC TITLE
DOCUMENT CONTROL SPECIFICATION

EFFECTIVE DATE : 2020-03-30

PRODUCT SPECIFICATION

TFT-LCD MODULE

Model No: FRD122B34001-B

For Customer's Acceptance	
Approved by	Comment

	Signature	Date
Prepared by	Wang Jin	2020.03.30
Checked by	Lan hui	2020.03.30
Approved by	Hu shizheng	2020.03.30

深圳市福瑞达显示技术有限公司

地址: 深圳市光明新区公明田寮第二工业区田荣路 68 号

Add:The 68th, Tianrong Road, Tianliao Community, Gongming Town, Guangming new district, shenzhen.

电话(Tel):0755-23422110

网址(Web):www.fridalcd.com

sales@fridalcd.com



Contents

No.	ITEM
1	Document Revision History
2	General Description
3	Outline Dimension
4	Interface Specification
5	Absolute Maximum Ratings
6	Electrical Specifications
7	Timing Characteristics
8	Power Supply Configuration
9	Optical Specification
10	Inspection Specifications
11	Reliability Test Items
12	Precautions



1. Document Revision History :

DOCUMENT REVISION	DATE	DESCRIPTION	PREPARED BY
A	2019-08-30	First Release.	
B	2020-03-30	Update contact information	

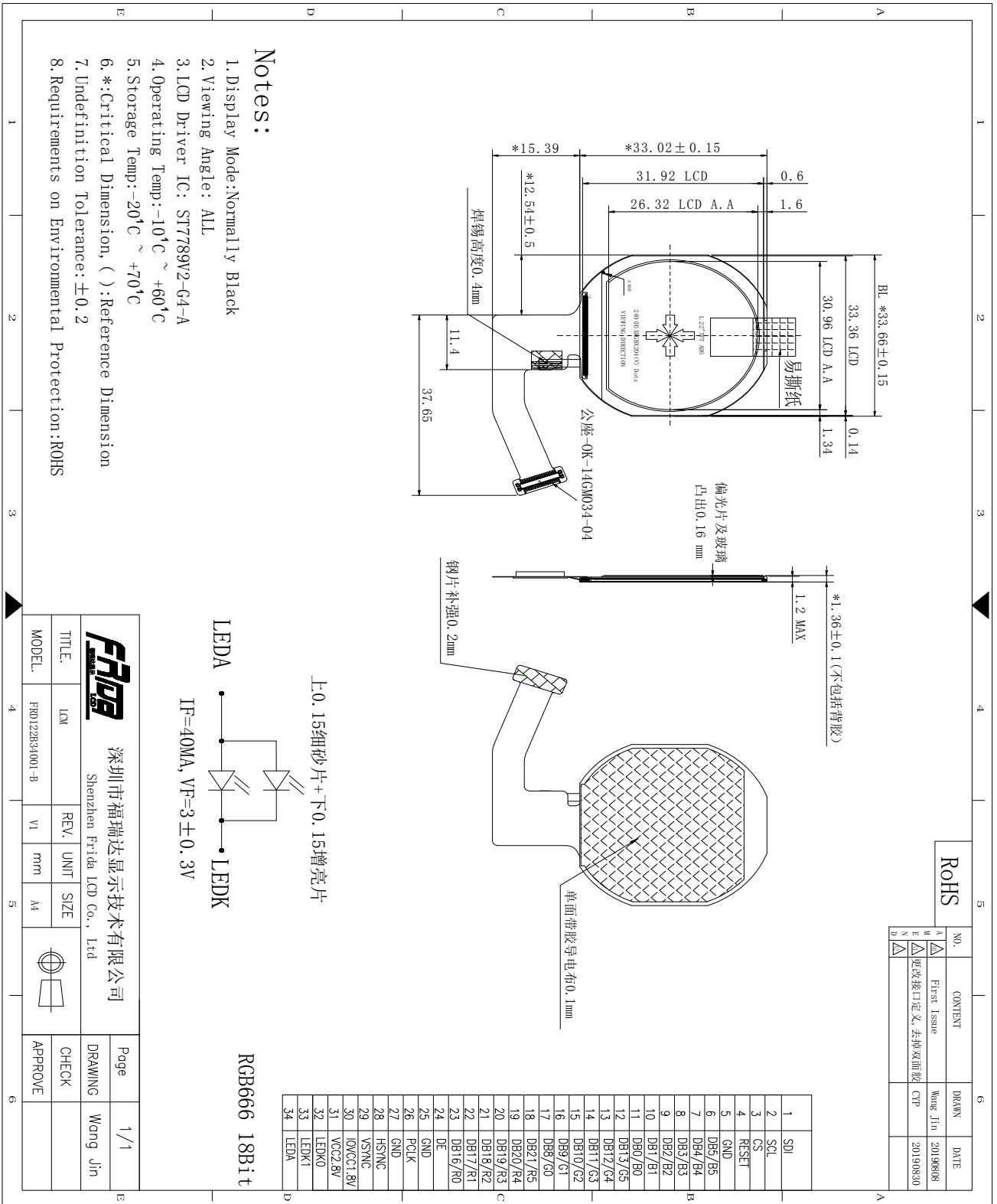


2. General Description

No	Item	Specification	Remark
1	Screen Size	1.22 inch	
2	Display Mode	Normally Black	
3	Resolution	240 × RGB × 204	
4	Active Area	30.96*26.32	
5	Outline Dimension	33.66*33.02*1.36	
6	Viewing Direction	ALL	
7	Driver IC	ST7789V2-G4-A	
8	Interface	RGB	
9	Back Light	White Led*2	
10	Touch Panel	-	



3. Outline Dimension





4. Interface Specification

Pin No	Symbol	Description	Note
1	SDI	Serial data input pin.	
2	SCL	Serial clock signal pin.	
3	CS	Chip selection signal pin.	
4	RESET	Reset Signal input pin.	
5	GND	Ground.	
6-11	B5-B0	Blue data (B0-LSB;B5-MSB)	
12-17	G5-G0	Green data (G0-LSB; G5-MSB)	
18-23	R5-R0	Red data (R0-LSB;R5-MSB)	
24	DE	Data enable signal for RGB interface operation.	
25	GND	Ground.	
26	PCLK	Dot clock signal for RGB interface operation.	
27	GND	Ground.	
28	HSYNC	Line synchronous signal for RGB interface operation.	
29	VSYNC	Frame synchronous signal for RGB interface operation.	
30	IOVCC	Power Supply For I/O.	
31	VCC	Power Supply For LCD.	
32	LEDK0	Power Supply For LED Backlight Cathode Input.	
33	KEDK1	Power Supply For LED Backlight Cathode Input.	
34	KEDA	Power Supply For LED Backlight Anode Input.	



5. Absolute Maximum Ratings

Electrical Maximum Ratings – for IC Only

Parameter	Symbol	Min.	Max.	Unit	Note
Power supply voltage (VCC)	VCC	-0.3	+4.6	V	1
Power supply voltage (IOVCC)	IOVCC	-0.3	+4.6	V	1

Note:

- 1.IOVCC,VCC, GND must be maintained.
- 2.The modules may be destroyed if they are used beyond the absolute maximum ratings.

6. Electrical Specifications

At Ta = 25 °C, VCC = 2.4V to 3.3V, IOVCC= 1.65V to 3.3V, GND=0V.

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Supply voltage (analog)	VCC-GND		2.4	2.75	3.3	V
Supply voltage (logic)	IOVCC-GND		1.65	1.8	3.3	V
Supply current (Logic & LCD)	ICC	VCC=2.75V	-	-		mA
Supply voltage of white LED backlight	VLED	Forward current =40mA Number of LED = 2	2.7	3	3.3	V

7. Timing Characteristics

7.4.4 RGB Interface Characteristics:

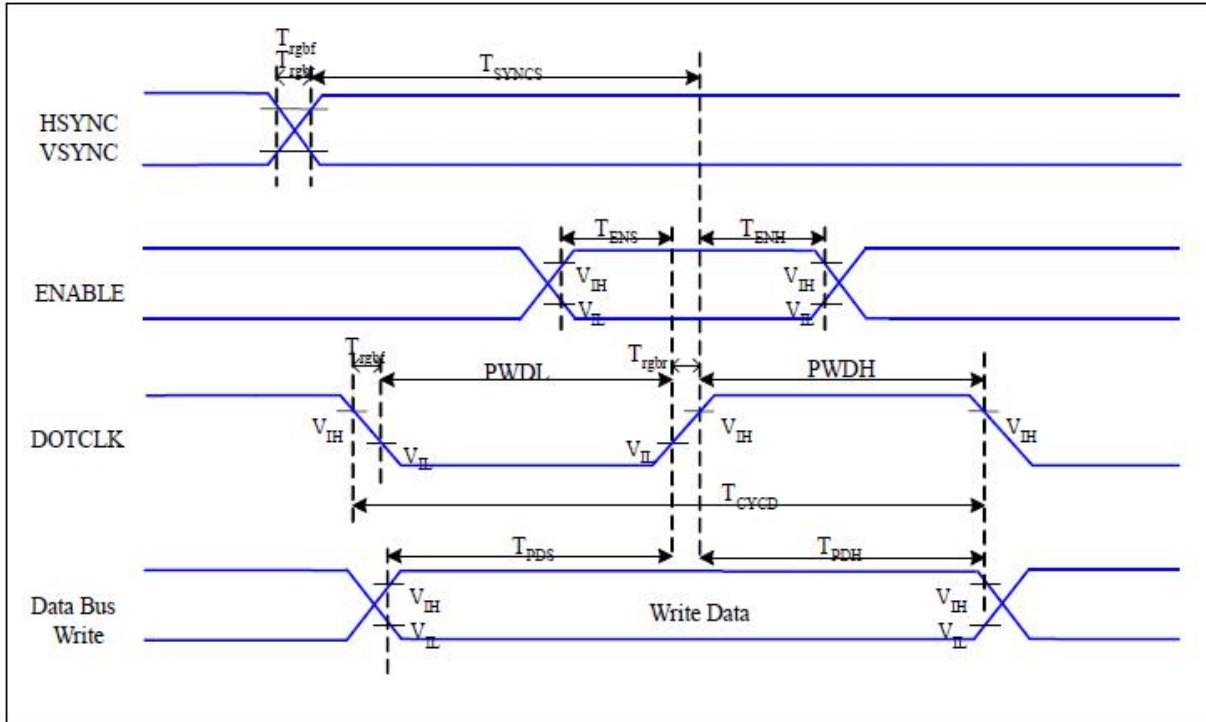


Figure 6 RGB Interface Timing Characteristics

VDDI=1.65 to 3.3V, VDD=2.4 to 3.3V, AGND=DGND=0V, Ta=25°C

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
HSYNC, VSYNC	T_{SYNCS}	VSYSNC, HSYNC Setup Time	30	-	ns	
ENABLE	T_{ENS}	Enable Setup Time	25	-	ns	
	T_{ENH}	Enable Hold Time	25	-	ns	
DOTCLK	PWDH	DOTCLK High-level Pulse Width	60	-	ns	
	PWDL	DOTCLK Low-level Pulse Width	60	-	ns	
	T_{CYCD}	DOTCLK Cycle Time	120	-	ns	
	Trghr, Trghf	DOTCLK Rise/Fall time	-	20	ns	
DB	T_{PDS}	PD Data Setup Time	50	-	ns	
	T_{PDH}	PD Data Hold Time	50	-	ns	

Table 7 18/16 Bits RGB Interface Timing Characteristics

	T_{ENH}	Enable Hold Time	35	-	ns
DOTCLK	PWDH	DOTCLK High-level Pulse Width	35	-	ns
	PWDL	DOTCLK Low-level Pulse Width	35	-	ns
	T_{CYCD}	DOTCLK Cycle Time	80	-	ns
	Trghr, Trghf	DOTCLK Rise/Fall time	-	10	ns
DB	T_{PDS}	PD Data Setup Time	35	-	ns
	T_{PDH}	PD Data Hold Time	35	-	ns

Table 8 6 Bits RGB Interface Timing Characteristics

7.4.5 Reset Timing:

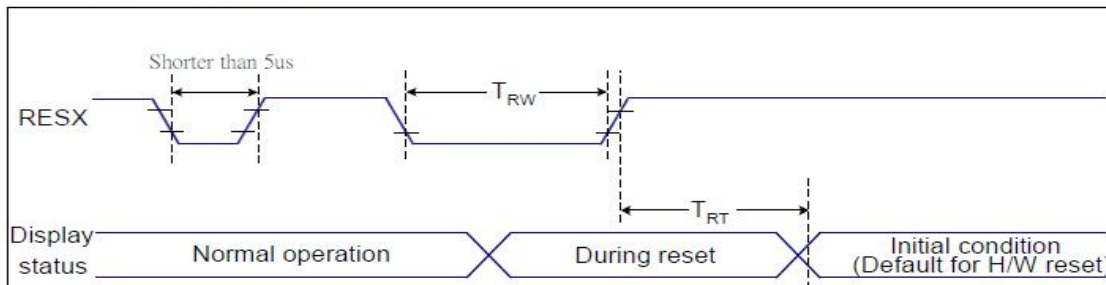


Figure 7 Reset Timing

VDDI=1.65 to 3.3V, VDD=2.4 to 3.3V, AGND=DGND=0V, Ta=25°C

Related Pins	Symbol	Parameter	MIN	MAX	Unit
RESX	TRW	Reset pulse duration	10	-	us
	TRT	Reset cancel	-	5 (Note 1, 5)	ms
			120 (Note 1, 6, 7)	ms	

Table 9 Reset Timing

Notes:

1. The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NVM (or similar device) to registers. This loading is done every time when there is HW reset cancel time (tRT) within 5 ms after a rising edge of RESX.

2. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

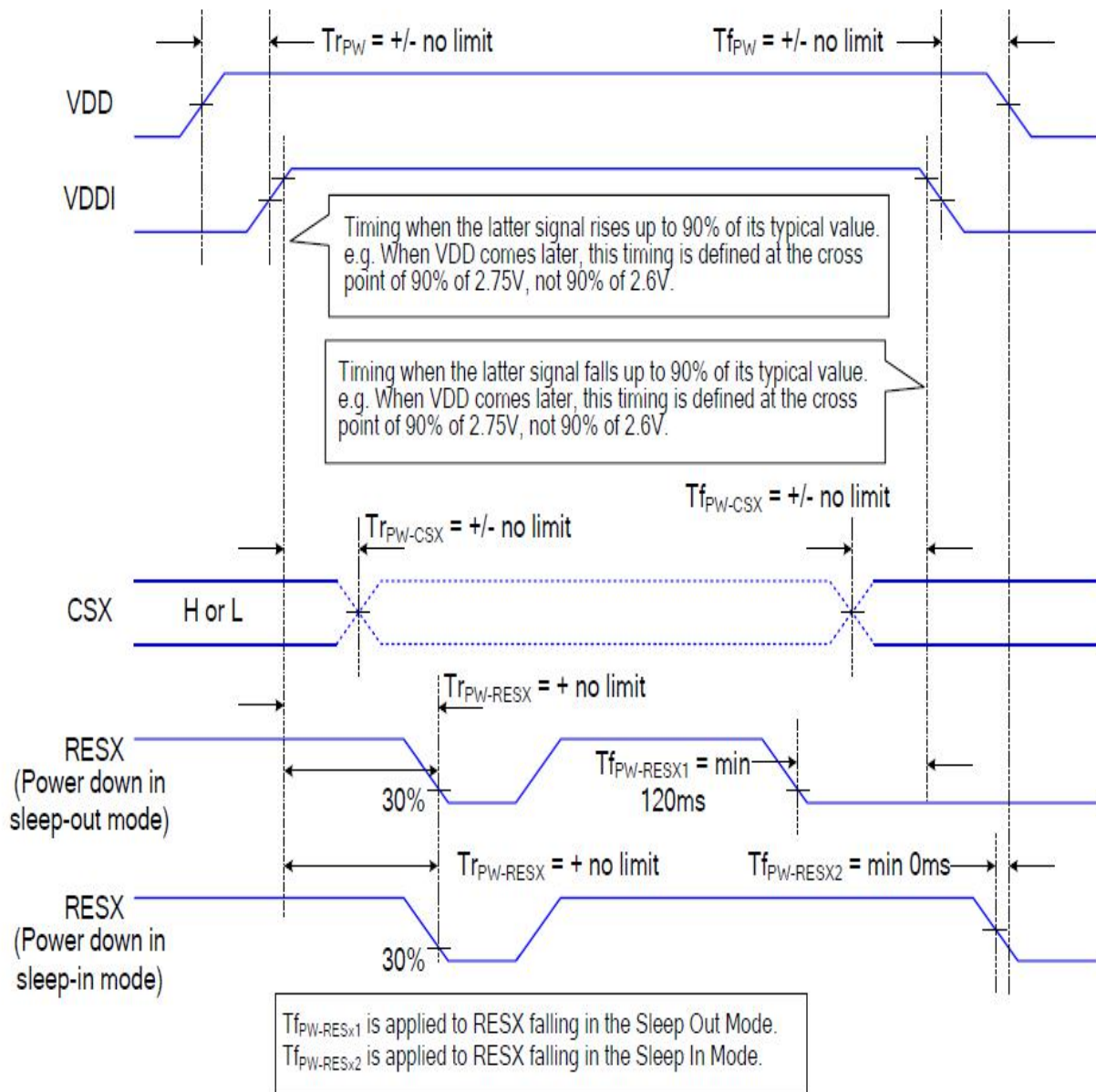
RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 9us	Reset
Between 5us and 9us	Reset starts

3. During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out -mode. The display remains the blank state in Sleep In -mode.) and then return to Default condition for Hardware Reset.

4. Spike Rejection also applies during a valid reset pulse as shown below:

8.Power Supply Configuration

The power on/off sequence is illustrated below





9.Optical Specification

Item 项目	Symbol 符号	Condition 条件	Min 最小值	Typ 典型值	Max 最大值	Unit 单位	Note 备注
Response time 响应时间	Tr+Tf	$\Theta=0^{\circ}$ $\emptyset=0^{\circ}$ Ta=25°C	-	30	35	ms	1
Contrast ratio 对比度	Cr		800	1000	-	-	2
Color gamut 饱和度	S(%)		-	70	-	%	-
Luminance uniformity 均匀度	δ WHITE		80	-	-	%	3
Viewing angle range 视角范围	Θ_{x+}	CR \geq 10 Ta=25°C	-	80	-	deg	4
	Θ_{x-}		-	80	-	deg	
	Θ_{y+}		-	80	-	deg	
	Θ_{y-}		-	80	-	deg	
LCM Luminance LCM 亮度	Lv	$\Theta=0^{\circ}$ $\emptyset=0^{\circ}$ Ta=25°C	-	350	-	Cd/m ²	5

Note1.Response time is the time required for the display to transition from White to black(Rise Time,Tr)and from black to white(Decay Time,Tf).For additional information see FIG1...

Note2.contrast Ratio(CR) is defined mathematically by the following formula ,For more information see FIG2.

Contrast Ratio(CR)=Average Surface Luminance with all white pixels/ Average Surface Luminance with all black pixels

Note3.The uniformity in surface luminance(WHITE) is determined by measuring luminance at each test position,and then dividing the maximum luminance of all white pixels by minimum luminance of all white pixels,For more information seeFIG2.

WHITE=Minimum Surface Luminance with all white pixels(P1,P2,.....)/Maximum Surface Luminance with all white pixels(P1,P2,.....)

Note4.Viewing angle is the angel at which contrast ratio is greater than a specific value.For TET module,the specific value of contrast ratio is 10.For monochrome and color stn module,the specific value of contrast ratio is2.The angles are determined for the horizontal or x axis and the vertical or y

axis with respect to the z axis which is normal to the LCD surface.For more information see FIG3
Note5. Surface luminance is the LCD surface luminance with all white pixels,For more information
see FIG2.

LV=Average Surface Luminance with all white pixels(P1,P2,.....)

FIG1. The definition of Response time

响应时间定义

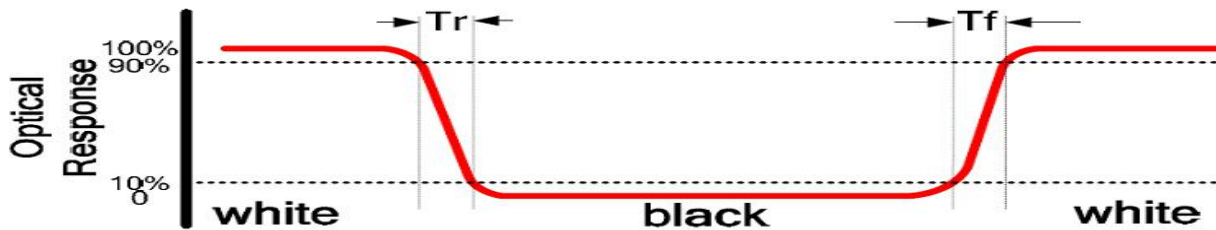


FIG2. Measuring method for Contrast ratio,surface luminance,Luminance

uniformity,CIE(X,Y)chromaticity.

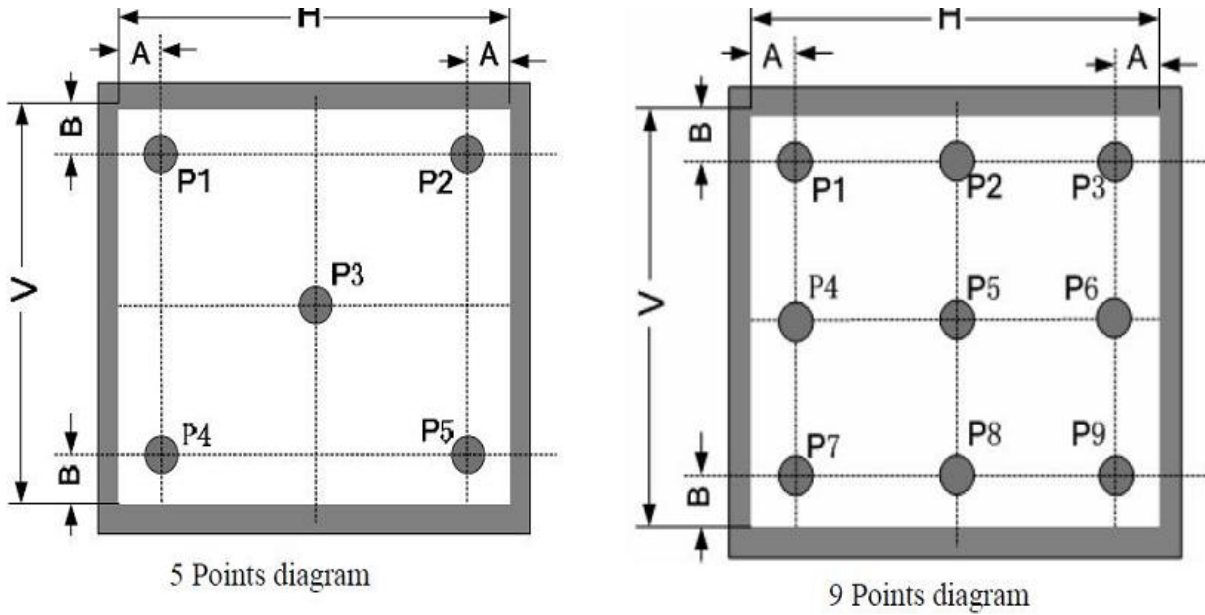
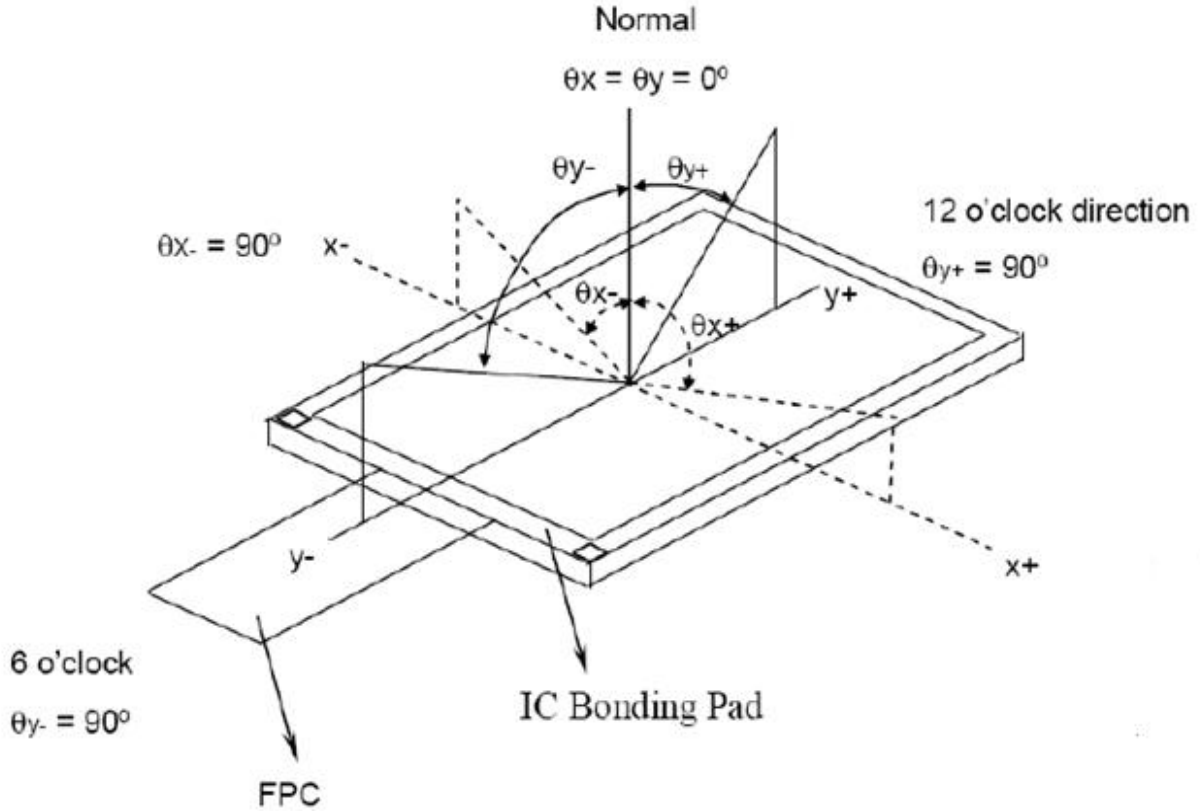


FIG3 The definition of viewing angle 视角定义



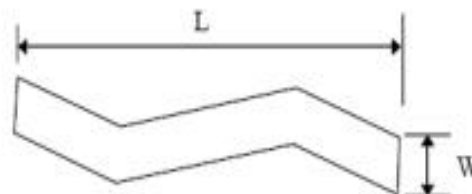
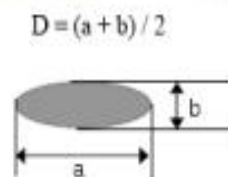
10. Inspection Specifications

10.1 Appearance inspection

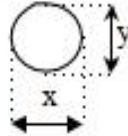
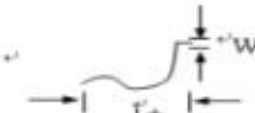
Item	Acceptable standards for defects	Defect level
Broken	Not allowed	critical defects
Cracks	Not allowed	critical defects
Insufficient UV glue entering	Not allowed	critical defects
Liquid crystal seal leakage	Not allowed	critical defects
Liquid crystal bubbles	Not allowed	critical defects
Surface scratch(mm)	$W \leq 0.02$, ignored	minor defects
	$0.02 < W \leq 0.03$ $L \leq 2, N \leq 2$	
	$0.03 < W \leq 0.05$ $L \leq 1, N \leq 1$	
	$0.05 < W$ Not allowed	
Black/white spot(mm)	$D \leq 0.1$, ignored; $0.1 < D \leq 0.15, N \leq 4$	minor defects
	$0.15 < D \leq 0.2, N \leq 2$; $0.2 < D$, Not allowed	
The seal pollution	Not allowed	minor defects
Liquid crystal residues	Not allowed	minor defects
Surface stains	Stains that cannot be cleaned or erased are not allowed	minor defects
size	Refer to the product specification corresponding to each product, overall size(including length, Width, thickness) or partial size exceeding the drawing size is not allowed	major defects

Remarks : 1)Surface scratches within 1.5mm of the glass edge are ignored;

2) D = diameter, L = length, W = width, N = qty;



10.2 Functional test criteria

Item	Judgment	Level																				
Display status	No Display、Incomplete image、line defect、wrong viewing angle、flickering、abnormal image、are not allowed	major defects																				
	Display color, judged by approved samples, Or by limited samples	minor defects																				
	MURA or the phenomenon that is unable to describe in words, judged by ND 5% or limited samples	minor defects																				
Spot(bright/dark)defect	Definition of spot defect: $\Phi = (x+y) / 2$ 	minor defects																				
	<table border="1"> <thead> <tr> <th rowspan="2">Size(mm)</th> <th colspan="2">acceptable qty</th> </tr> <tr> <th>Active area</th> <th>View area</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.1$</td> <td colspan="2">ignored</td> </tr> <tr> <td>$0.1 < \Phi \leq 0.15$</td> <td colspan="2">2 (gap ≥ 5)</td> </tr> <tr> <td>$0.15 < \Phi \leq 0.2$</td> <td colspan="2">1</td> </tr> <tr> <td>$0.2 < \Phi$</td> <td colspan="2">Not allowed</td> </tr> </tbody> </table>		Size(mm)	acceptable qty		Active area	View area	$\Phi \leq 0.1$	ignored		$0.1 < \Phi \leq 0.15$	2 (gap ≥ 5)		$0.15 < \Phi \leq 0.2$	1		$0.2 < \Phi$	Not allowed				
	Size(mm)			acceptable qty																		
			Active area	View area																		
	$\Phi \leq 0.1$		ignored																			
	$0.1 < \Phi \leq 0.15$		2 (gap ≥ 5)																			
$0.15 < \Phi \leq 0.2$	1																					
$0.2 < \Phi$	Not allowed																					
Black/white line	Definition of line defect: L: length, W: width 	minor defects																				
	<table border="1"> <thead> <tr> <th colspan="2">Size(mm)</th> <th colspan="2">Acceptable qty</th> </tr> <tr> <th>W(width)</th> <th>L(length)</th> <th>Active area</th> <th>View area</th> </tr> </thead> <tbody> <tr> <td>$W \leq 0.03$</td> <td>ignored</td> <td>ignored</td> <td>ignored</td> </tr> <tr> <td>$0.03 < W \leq 0.05$</td> <td>$L \leq 2.0$</td> <td>3</td> <td></td> </tr> <tr> <td>$W > 0.05$</td> <td>-</td> <td>Not allowed</td> <td></td> </tr> </tbody> </table>		Size(mm)		Acceptable qty		W(width)	L(length)	Active area	View area	$W \leq 0.03$	ignored	ignored	ignored	$0.03 < W \leq 0.05$	$L \leq 2.0$	3		$W > 0.05$	-	Not allowed	
	Size(mm)		Acceptable qty																			
	W(width)		L(length)	Active area	View area																	
	$W \leq 0.03$		ignored	ignored	ignored																	
$0.03 < W \leq 0.05$	$L \leq 2.0$	3																				
$W > 0.05$	-	Not allowed																				



11. Reliability Test Items

Item	Test Condition	Criterion
High Temperature Storage	70 °C, 48 hrs	Note1,Note2
Low Temperature Storage	-20 °C, 48 hrs	
High Temp. & High Humidity Storage	40 °C, 80% RH, 48hrs	
Thermal Shock (Static)	-20°C, 30 min /70°C, 30 min, 20 cycles	
High Temperature Operation	60 °C, 48 hrs	
Low temperature Operation	-10 °C, 48 hrs	

Note1: Evaluation should be tested after storage at room temperature for two hours.

Note2:

Pass: Normal display image no line defect.

Fail: No display image, or line defects.

Partial transformation of the module parts should be ignored.

12. Precautions

Please pay attentions to the followings as using the LCD module.

Handling

- Do not apply strong mechanical stress like drop, shock or any force to LCD module. It may cause improper operation, even damage.
- Because the polarizer is very fragile and easy to be damaged, do not hit, press or rub the display surface with hard materials.
- Do not put heavy or hard material on the display surface, and do not stack LCD modules.
- If the display surface is dirty, please wipe the surface softly with cotton swab or clean cloth.
- Avoid using Ketone type materials (e.g. Acetone), Toluene, Ethyl acid or Methyl chloride to clean



the display surface. It might damage the touch panel surface permanently. The recommended solvents are water and Isopropyl alcohol.

- (f) Wipe off water droplets or oil immediately.
- (g) Protect the LCD module from ESD. It will damage the LSI and the electronic circuit.
- (h) Do not touch the output pins directly with bare hands.
- (i) Do not disassemble the LCD module.
- (j) Do not lift the FPC of Touch Panel.

Storage

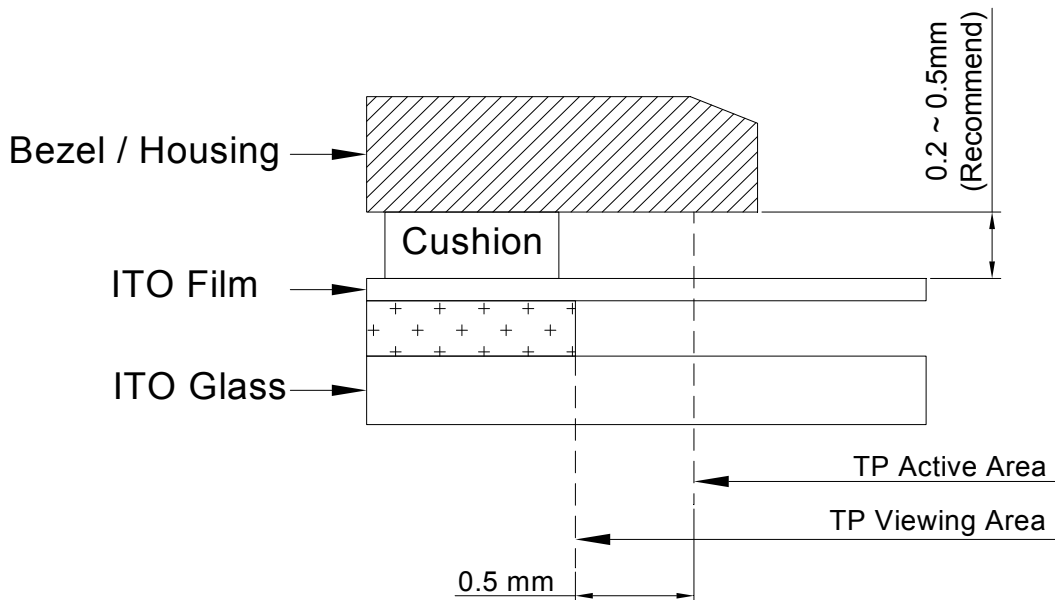
- (a) Do not leave the LCD modules in high temperature, especially in high humidity for a long time.
- (b) Do not expose the LCD modules to sunlight directly.
- (c) The liquid crystal is deteriorated by ultraviolet. Do not leave it in strong ultraviolet ray for a long time.
- (d) Avoid condensation of water. It may cause improper operation.
- (e) Please stack only up to the number stated on carton box for storage and transportation. Excessive weight will cause deformation and damage of carton box.

Operation

- (a) When mounting or dismounting the LCD modules, turn the power off.
- (b) Protect the LCD modules from electric shock.
- (c) The Driver IC control algorithms stated above should always obeyed to avoid damaging the LSI and electronic circuit.
- (d) Be careful to avoid mixing up the polarity of power supply for backlight.
- (e) Absolute maximum rating specified above has to be always kept in any case. Exceeding it may cause non-recoverable damage of electronic components or, nevertheless, burning.
- (f) When a static image is displayed for a long time, remnant image is likely to occur.
- (g) Be sure to avoid bending the FPC to an acute shape, it might break FPC.
- (h) Most of the touch screens have air vent to equalize the inside air pressure to the outside one. The air vent must be open and liquid contact must be avoided as the liquid may be absorbed if the liquid is accumulated near the air vent.
- (i) For the fragility of ITO film, it should avoid to use too tapering pen as the input material.

Touch Panel Mounting Notes

- If a cushion is used between bezel/housing and film must be choose as free as enough to absorb the expansion and contraction to avoid the distortion of film.
- The cushion must be placed out of the Viewing Area.
- Bezel/Housing edge must be posited between Key Area and Viewing Area. The edge enters the Key Area may cause unexpected input if the gap is too narrow or foreign particles like dusts exist between Bezel/Housing and ITO film.
- Mounting example:



The corner part has conductivity. Do not touch any metal part after mounting.

Others

- If the liquid crystal leaks from the panel, it should be kept away from the eyes or mouth.
- For the fragility of polarizer, it is recommended to attach a transparent protective plate over the display surface.
- It is recommended to peel off the protection film on the polarizer slowly so that the electrostatic charge can be minimized.